



**Integral University, Lucknow**  
**Department of Electronics and Communication Engineering**  
**Study and Evaluation Scheme**

**Program: B.Tech. Electronics and Communication Engineering**

**Semester III**

S. No.	Course code	Course Title	Type of Paper	Period Per hr/week/sem			Evaluation Scheme				Max. Marks	Credit	Total Credits	Employability	Entrepreneurship	Skill Development	Gender Equality	Environment & Sustainability	Human Value	Professional Ethics	United Nations Sustainable Development Goals (SDG)
				L	T	P	CT	TA	Total	ESE											
<b>THEORIES</b>																					
1	MT201	Engineering Mathematics III	Core	3	1	0	40	20	60	40	100	3:1:0	4	√	√						
2	EC201	Electronic Devices and Circuits	Core	3	1	0	40	20	60	40	100	3:1:0	4	√	√						8,11
3	EC202	Electromagnetic Field Theory	Core	3	1	0	40	20	60	40	100	3:1:0	4	√	√		√				8,11
4	EC203	Circuit Theory	Core	3	1	0	40	20	60	40	100	3:1:0	4	√	√						4,12
5	ES202	Disaster Management	Core	2	1	0	40	20	60	40	100	2:1:0	3	√	√		√				3,9,11,17
6	CS203	Cyber Law and Information Security	Core	2	1	0	40	20	60	40	100	2:1:0	3	√	√			√	√		
7	BM226	Human Values & Professional Ethics**	Core	3	0	0	-	-	-	50	50	3:0:0	0				√		√	√	
<b>PRACTICAL</b>																					
8	EC204	Electronic Circuit Simulation Lab.	Core	0	0	2	40	20	60	40	100	0:0:2	1	√	√	√					8,12
9	EC205	Electronics Workshop	Core	0	1	2	40	20	60	40	100	0:1:2	2	√	√	√					8,9
10	EC206	Electronic Devices and Circuits Lab.	Core	0	0	2	40	20	60	40	100	0:0:2	1	√	√	√					8,11
11	EC207	Circuit Theory Lab.	Core	0	0	2	40	20	60	40	100	0:0:2	1	√	√	√					4,12
<b>Total</b>				<b>17</b>	<b>7</b>	<b>8</b>	<b>400</b>	<b>200</b>	<b>600</b>	<b>400</b>	<b>1000</b>	<b>34</b>	<b>27</b>								

\*\* Audit Course (BM226): This is a compulsory audit course in which a student is required to clear this paper with 50% passing marks and these marks will not be included in result.